

Notice of References Cited	Application/Control No. 10/057,205	Applicant(s)/Patent Under Reexamination QUINLAN ET AL.	
	Examiner Scott B. Geyer	Art Unit 2829	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US- 5027253	06-1991	LAUFFER et al.	361/321
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM Technical Disclosure Bulletin, "Clipped Decoupled Twin-Carrier Module for IC Memory Chips", January 1, 1985, page number 4857-4858
	V	IBM Technical Disclosure Bulletin, "Decoupling Capacitor Structure to Reduce FET Output Driver Switching Noise", December 1, 1987, page 167-168
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.